

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. - 2. (canceled).

3. (currently amended): Clock-pulse supply unit ~~according to Claim 2~~, comprising:

a phase detector for comparing the phase of a first clock pulse with the phase of a second clock pulse;

a first receiver unit for receiving the first clock pulse and relaying the first clock pulse to the phase detector;

a second receiver unit which has the same parameters as the first receiver unit and serves to receive the second clock pulse and relay the second clock pulse to the phase detector;

an oscillator for generating the second clock pulse;

a transmitter unit connected between the oscillator and the second receiver unit in such a way that the second clock pulse generated in the oscillator is supplied to the second receiver unit via the transmitter unit; and

a changeover switch with one input and two outputs ~~is provided~~,

wherein the input of the changeover switch is connected to the output of the transmitter unit, and an output of the changeover switch is connected to an artificial line.

4. (currently amended): Clock-pulse supply unit according to Claim ~~2~~, ~~comprising 3~~,
wherein the oscillator is ~~designed as a~~ voltage-controlled oscillator whose control voltage is
dependent on the output signal of the phase detector .

5. (currently amended): Clock-pulse supply unit ~~according to Claim 2~~, comprising:
a phase detector for comparing the phase of a first clock pulse with the phase of a second
clock pulse;

a first receiver unit for receiving the first clock pulse and relaying the first clock pulse to
the phase detector;

a second receiver unit which has the same parameters as the first receiver unit and serves
to receive the second clock pulse and relay the second clock pulse to the phase detector;

an oscillator for generating the second clock pulse;

a transmitter unit connected between the oscillator and the second receiver unit in such a
way that the second clock pulse generated in the oscillator is supplied to the second receiver unit
via the transmitter unit; and

a divider is provided, connected between the oscillator and the phase detector, ~~and~~
wherein the divider and the second receiver unit ~~which~~ have a control input for active and
passive switching of the divider and the second receiver unit.

6. (currently amended): Clock-pulse supply unit according to Claim ~~1~~, ~~comprising 3~~,
wherein the first and the second receiver unit have the same time delay.

7. (currently amended): Clock-pulse supply unit ~~according to Claim 1~~, comprising:

a phase detector for comparing the phase of a first clock pulse with the phase of a second clock pulse;

a first receiver unit for receiving the first clock pulse and relaying the first clock pulse to the phase detector;

a second receiver unit which has the same parameters as the first receiver unit and serves to receive the second clock pulse and relay the second clock pulse to the phase detector; and

a reference clock-pulse changeover switch ~~is provided whose~~ having an output is connected to the output of the first receiver unit, ~~and wherein~~ the reference clock-pulse changeover switch and the first receiver unit ~~which~~ have a control input for active and passive switching of the reference clock-pulse changeover switch and the first receiver unit.

8. (currently amended): Clock-pulse supply unit according to Claim 3, ~~comprising~~ wherein the artificial line is suitable for simulation of a system clock-pulse bus.

9. (currently amended): Clock-pulse supply unit according to claim 3, ~~comprising~~ wherein the first receiver unit is suitable for receiving a system clock pulse from a system clock-pulse bus, the changeover switch is suitable for switching the clock pulse received from the transmitter unit to the system clock-pulse bus, and the reference clock-pulse changeover switch is suitable for supplying to the phase detector at least one reference clock pulse, the frequency of which is lower than the frequency of the system clock pulse.

10. (canceled).

11. (new): Clock-pulse supply unit according to claim 7, wherein the first receiver unit is suitable for receiving a system clock pulse from a system clock-pulse bus, the changeover

switch is suitable for switching the clock pulse received from the transmitter unit to the system clock-pulse bus, and the reference clock-pulse changeover switch is suitable for supplying to the phase detector at least one reference clock pulse, the frequency of which is lower than the frequency of the system clock pulse.